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## \*BIBDATASHEET\*

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Bib Data Sheet

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## APPLICANTS

Nobuaki Hashimoto, Suwa-shi, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 10/254,600 09/26/2002 PAT 6,730,589  
which is a DIV of 09/117,510 04/22/1999 PAT 6,475,896  
which is a 371 of PCT/JP97/04437 12/04/1997

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 8-356880 12/26/1996  
JAPAN 9-91449 03/26/1997  
JAPAN 8-339045 12/04/1996

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/04/2004

|  |  |                              |                         |                      |                            |
|--|--|------------------------------|-------------------------|----------------------|----------------------------|
| Foreign Priority claimed<br>35 USC 119 (a-d) conditions met<br>Verified and Acknowledged | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance<br>Examiner's Signature Initials | STATE OR<br>COUNTRY<br>JAPAN | SHEETS<br>DRAWING<br>23 | TOTAL<br>CLAIMS<br>1 | INDEPENDENT<br>CLAIMS<br>1 |
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## ADDRESS

OLIFF & BERRIDGE, PLC  
P.O. Box 19928  
Alexandria , VA  
22320

## TITLE

ELECTRONIC COMPONENT AND SEMICONDUCTOR DEVICE, METHOD OF MAKING THE SAME AND  
METHOD OF MOUNTING THE SAME, CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT

|            |   |   |
|------------|---|---|
| FILING FEE | FEES: Authority has been given in Paper | <input type="checkbox"/> All Fees<br><input type="checkbox"/> 1.16 Fees ( Filing )<br><input type="checkbox"/> 1.17 Fees ( Processing Ext. of |
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